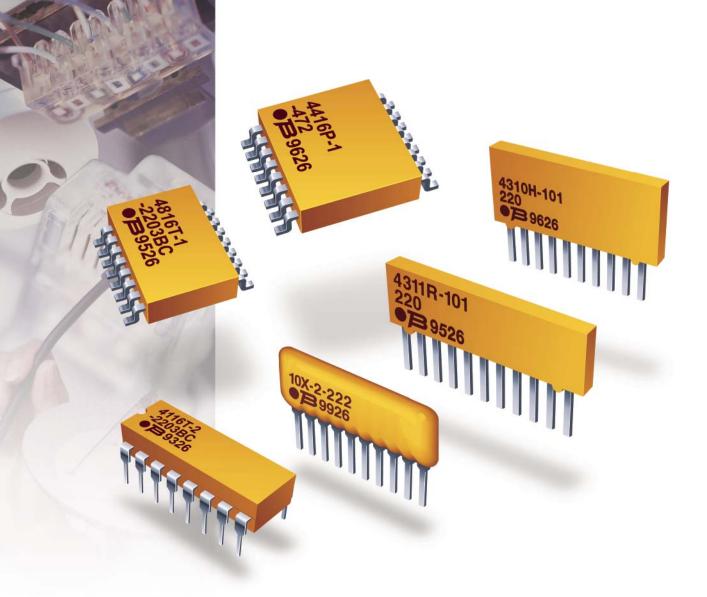
# **Bourns**<sup>®</sup> **Resistor Networks**

Short Form Catalog





## What is a Resistor Network?

A resistor network is a single package that contains two or more resistors. The package will include multiple leads by which the network can be made part of a larger circuit.



Resistor Network

## Why Networks?

- Board Space Reduction
- Improved Reliability
- Fewer Solder Connections
- Improved TCR Tracking
- Improved Resistor Tolerance Matching
- Lower Installed Cost
- Part Count Reduction
- Increased Factory Throughput

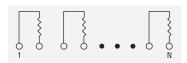
## **Network Solutions**

- Types Available: Thick-Film Resistor and RC Networks, Precision Thin-Film-on-Ceramic Resistors, Thin-Film-on-Silicon (please refer to Bourns<sup>®</sup> Integrated Passive & Active Devices Short Form Catalog)) Resistors and Capacitors.
- Mounting: Surface Mount and Through-hole.
- Pin Counts: 4 to 20.
- Functions: EMI/RFI Filters, T-Filters, IEEE Filters, Memory Termination, RC Terminators, AC Terminators, V.35 Terminators, ECL Termination, R2R Ladders, SCSI Terminators, Pull Up/Pull Down, Current Limiting.

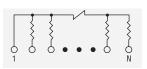
- **Standard Circuits:** Bussed, Isolated or Dual Terminator.
- Thick-film and Precision Thin-film Package Types: Through-hole: Conformal SIP, Molded SIP, Molded DIP. Surface Mount: Wide Body SOIC, Medium Body SOIC, Narrow Body SOIC, QSOP (Narrow Body SOIC and QSOP - Silicon substrate).
- *Packaging: Tape and Reel, Ammo Pack, Plastic Tubes, Bulk.*
- Applications: Bourns<sup>®</sup> Networks benefit spacesensitive applications by minimizing space, reducing costs, increasing board yields and reliability by reducing component count.

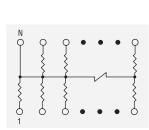
## **Standard Circuits**

Isolated





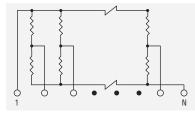


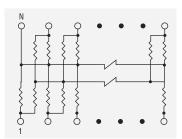


▶ 0

#### **Dual Terminator**

Not available in thin-film





## **Application Specific Circuits**

- AC Terminators
- CMOS Terminators
- EMI/RFI Filters
- R2R Ladder Networks
- Single Ended SCSI Terminators
- Differential SCSI Terminators

## **Common Characteristics of Resistor Networks**

#### Tolerance (Absolute Tolerance)

The permitted variation from the specified value of a single resistor. Defined as a  $\pm$  percentage of the ideal value, e.g.  $\pm$  5 %.

#### **Ratio Tolerance**

The permitted variation from the specified ratio formed by two or more values within a resistor network. Defined as a  $\pm$  percentage of the ideal ratio, e.g.  $\pm$  0.1 %.

#### **Networks Capabilities**

#### **Custom Networks Guidelines**

- All standard catalog package styles
- *Minimum order as low as 2,000 pieces for resistor networks*
- Within capability matrix guidelines
- Special features include:
- $\cdot$  Pin length As short as 0.100 ", as long as 0.200 "
- $\cdot$  Special ratios As tight as +/- 0.05 %
- · Custom marking
- · Custom values/circuits

## SMT – DIP Package

Thick-Film DIP: Two models

- 44xxP (0.295 inch wide)
- 48xxP (0.220 inch wide)

- **Thin-Film DIP:** *Two models* • 44xxT (0.295 inch wide)
- 48xxT (0.220 inch wide)

### **Through-Hole SIP & DIP Packages**

#### Thick-Film DIP:

• 41xxR

- Thin-Film DIP & SIP: Two models
- 41xxT (DIP)
- 43xxT,S,K (SIP, Three heights)

Thick-Film SIP: Two models (Three heights per model)

- 46xxX, M, H Conformal Coat
- 43*xxR*, *M*, *H Molded*

**TCR** (Absolute TC; Temperature Coefficient of Resistance) The expected change in value of a single resistor in response to a change in temperature. Usually defined in units of PPM/°C. For reference:  $\pm 100 \text{ PPM/°C} = 0.01 \%/°C.$ 

#### TCR Tracking (Ratio TC)

The expected change in the ratio formed by two or more resistors in response to a change in temperature. Usually defined in units of PPM/°C.

#### **Product Selection Thick vs. Thin-Film**

	Thick-film (Ceramic)	Thin-film (Ceramic)	Thin-film (Silicon)
Resistance Range	1	2	3
Power	1	2	2
Broadest Package Selection	1	2	3
Smallest Packages	2	2	1
Precision	3	2	1
Cost	1	3	2
TCR/Tracking	3	1	2
Density	2	3	1
Component Integration	1	2	3

Key: 1= best

Note: Please refer to Integrated Passive & Active Devices Short Form Catalog for Thin-Film-on-Silicon

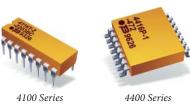




#### **Thick-Film Resistors SIP**

4600 Series

Series	Mounting	Available Number of Pins	Max. Lead-Lead Spacing (mm)	Resistance Range (ohms)	Max. Voltage (Volts)	Absolute TCR (50 to 2.2M ohms)	Ratio TCR Standard	Max. Package Power (W)	Resistor Power Rating (W)
Molded Encapsulate									
4300R	TH	11	2.54	10 to 10M	100	±100 PPM/°C	±100 PPM/°C	1.38	.2030
4300M	TH	10	2.54	10 to 10M	100	±100 PPM/°C	±100 PPM/°C	1.50	.2540
4300H	TH	10	2.54	10 to 10M	100	±100 PPM/°C	±100 PPM/°C	2.00	.3050
				Conformal Coa	at Encapsulate				
4600X	TH	14	2.54	10 to 10M	100	±100 PPM/°C	±100 PPM/°C	1.75	.2030
4600M	TH	14	2.54	10 to 10M	100	±100 PPM/°C	±100 PPM/°C	2.10	.2540
4600H	TH	14	2.54	10 to 10M	100	±100 PPM/°C	±100 PPM/°C	2.80	.3050





#### **Thick-Film Resistors DIP**

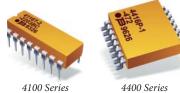
Series	Mounting	Available Number of Pins	Max. Lead-Lead Spacing (mm)	Resistance Range (ohms)	Max. Voltage (Volts)	Absolute TCR (50 to 2.2M ohms)	Ratio TCR Standard	Max. Power (Watts)	Resistor Power Rating (W)
	Molded Encapsulate								
4100R	ТН	20	2.54	10 to 10M	100	±100 PPM/°C	±100 PPM/°C	2.80	.12525
4400P	SMT	20	1.27	10 to 2.2M	50	±100 PPM/°C	±100 PPM/°C	2.00	.11516
4800P	SMT	20	1.27	10 to 2.2M	50	±100 PPM/°C	$\pm 100 \text{ PPM/°C}$	1.60	.0816



#### **Thin-Film Resistors on Ceramic SIP**

4300 Series

Series	Mounting	Available Number of Pins	Max. Lead-Lead Spacing (mm)	Resistance Range (ohms)	Max. Voltage (Volts)	Absolute TCR (within Resistance Range)	Ratio TCR Standard	Max. Power (Watts)	Resistor Power Rating (W)
	Molded Encapsulate								
4300T	TH	11	2.54	20 to 200K	50	±100 - ±25 PPM/°C	±5 PPM/°C	1.38	.1018
4300S	TH	11	2.54	20 to 200K	50	±100 - ±25 PPM/°C	±5 PPM/°C	1.50	.1220
4300K	TH	11	2.54	20 to 200K	50	±100 - ±25 PPM/°C	±5 PPM/°C	2.00	.1525





### **Thin-Film Resistors on Ceramic DIP**

4100 Series

4800 Series

Series	Mounting	Available Number of Pins	Max. Lead-Lead Spacing (mm)	Resistance Range (ohms)	Max. Voltage (Volts)	Absolute TCR (within Resistance Range)	Ratio TCR Standard	Max. Power (Watts)	Resistor Power Rating (W)
				М	olded Encaps	ulate			
4100T	TH	20	2.54	50 to 100K	50	±100 - ±25 PPM/°C	±5 PPM/°C	2.80	.1220
4400T	SMT	20	1.27	10 to 150K	50	±100 - ±25 PPM/°C	±5 PPM/°C	2.00	.1015
4800T	SMT	20	1.27	100 to 100K	50	±100 - ±25 PPM/°C	±5 PPM/°C	1.60	.0810



## How to Order

4400 Series



4800 Series

<b>4400P</b> (DIP Surface Mount, Wide Body) / <b>4800P</b> (DIP Surface Mount, Medium Body)
44xx/48xxP - 1- 152
Model
• 44 = SMD SOL Package
• 48 = SOM Package
Number of Pins
• 16/20 (44 Series)
• 14/16/18/20 (48 Series)
Туре
• P = Thick-Film ; T = Thin-Film
Electrical Configuration
• 1 or 4 = lolated
• 2 = Bussed
• 3 = Dual Terminator
Resistance Code
First 2 digits are significant; 3rd digit is the number of zeroes to follow
(Thin film only) First 3 digits are significant; 4th digit is the
number of zeroes to follow
Resistance Tolerance
• Blank = 2 %
• F = 1 % (100 to 1 Megohm)
• D = 0.5 % (100 to 1 Megohm)
Terminations
• LF = Tin-Plated (Lead Free)

• Blank = Solder-Plated (Tin Lead); 63 % Tin 37 % Lead



4100 Series

4100R (DIP Through-hole)	
41XX R - 1 - 152	
Model	1
• 41 = Molded DIP	
Number of Pins	
• 08/14/16/18/20	
Physical Configuration	
• R= Thick-Film Low Profile	
Electrical Configuration	
• 1 = Isolated	
• 2 = Bussed	
• 3 = Dual Terminator	
Resistance Code	
• First 2 digits are significant; 3rd digit is the number of zeroes to follow	
Resistance Tolerance	
• Blank = 2 %	
• F = 1 % (100 to 1 Megohm)	
• D = 0.5 % (100 to 1 Megohm)	
Terminations	
• LF = Tin-Plated (Lead Free)	
Disale Calden Distant (Tis Land) 00.0( Tis 10.0( Land	

• Blank = Solder-Plated (Tin Lead); 90 % Tin 10 % Lead





4600 Series

	46XX	(Н-	· 101 - 2	22
Model	t t	1	1	1 1
• 43 = Molded SIP				
• 46 = Conformal SIP				
Number of Pins				
• 6 to 11 (4300); 4 to 14 (4600)				
Physical Configuration				
• H = High Profile				
• M = Medium Profile				
• $X = Low Profile$ (4600) or $R = Low Profile$ (43	00)			
Electrical Configuration				
• 101/AP1 = Bussed / Bussed Ammo				
<ul> <li>102/AP2 = Isolated / Isolated Ammo</li> </ul>				
• 104/AP4 = Dual Terminator / Dual Ammo				
Resistance Code				]
• First 2 digits are significant; 3rd digit is the n	umber of ze	eroes t	o follow	/
Resistance Tolerance				
• Blank = 2 %				
• F = 1 % (100 to 1 Megohm)				
• D = 0.5 % (100 to 1 Megohm)				
Terminations				
• LF = Tin-Plated (Lead Free) (except 104 & AP	24)			
• L = Tin–Plated Lead Free (Only 104 & AP4)				
Plank - Solder Plated (Tip Lead): 00 % Tip 1	0.0/1 and $(4)$	2001		

• Blank = Solder-Plated (Tin Lead); 90 % Tin 10 % Lead (4300); 96.5 % Tin 3.0 % Silver 0.5 % Copper (4600)



#### 520/04-101 0-74-025 520/04-101 520/04-100 500/04-100 500/04-1

4300 Series



4400 Series



4800 Series

How to Order
Thin-Film
<b>4100T</b> (Thin-Film Molded DIP)
4300T, S, K (Thin-Film Molded SIP Low/Medium/High Profile)
<b>4400T</b> (Wide Body)
4800T (Medium Body)
4116 T - 2 - 2222 F A B
Model t t t t t t t t
• 41 = Molded DIP
• 43 = Molded SIP
• 44 = SOL Wide Body Gull Wing
• 48 = SOM Medium Body Gull Wing
Number of Pins
• 8/14/16/18/20 (4100T Series)
• 4/6/8/9/10/11 (4300T Series)
• 16/20 (4400T Series)
• 14/16/18/20 (4800T Series)
Physical Configuration
•T = Thin Profile
4300:
• K = High Profile
• M = Medium Profile
•T = Low Profile
Electrical Configuration
• 1 = Isolated
• 2 = Bussed
4100T Series:
• 101 = Isolated
• 102 = Bussed
• 106 = Series
Resistance Code
• First 3 digits are significant; 4th digit is the number of zeros to follow
Absolute Tolerance Code           • B = 0.1 %
• $B = 0.1 \%$ • $D = 0.5 \%$
• $D = 0.5\%$ • $F = 1\%$
Temperature Coefficient Code (PPM/°C)
$\bullet A = 100$
• $B = 50$
• C = 25
Ratio Tolerance (Optional)
• $A = 0.05 \%$ to $R1$
• $B = 0.1 \%$ to $R1$
• $D = 0.5 \%$ to R1
Terminations —
• L = Tin-Plated Lead Free

• Blank = Solder-Plated (Tin Lead); 90 % Tin 10 % Lead (4100 & 4300); 63 % Tin 37 % Lead (4400 and 4800)



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