

Features

- RoHS compliant*
- Protects four I/O lines
- Ultra-low capacitance ~ 0.55 pF
- ESD protection >30 kV
- Surge protection



This model is obsolete.

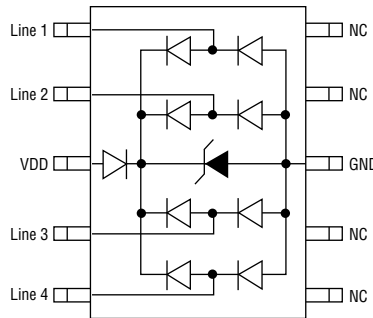
See [Product Obsolescence Memo](#) for possible replacement part no.

CDMSP10-0504M – Surface Mount TVS Diode Array

General Information

The CDMSP10-0504M device provides ESD, EFT and Surge protection for high speed data ports meeting IEC 61000-4-2 (ESD), IEC 61000-4-4 (EFT) and IEC 61000-4-5 (Surge) requirements. The Transient Voltage Suppressor array, protecting up to 4 data lines, offers a Working Peak Reverse Voltage of 5 V and Minimum Breakdown Voltage of 6 V.

The MSOP-10L packaged device will mount directly onto the industry standard MSOP-10L footprint. Bourns® Chip Diodes conform to JEDEC standards, are easy to handle with standard pick and place equipment and their flat configuration minimizes roll away.



Additional Information

Click these links for more information:



[PRODUCT SELECTOR](#)



[TECHNICAL LIBRARY](#)



[INVENTORY](#)



[SAMPLES](#)



[CONTACT](#)

Absolute Maximum Ratings, $T_A = 25^\circ\text{C}$ (Unless Otherwise Noted)

Parameter	Symbol	Value	Unit
Operating Supply Voltage	V_{DC}	6	V
DC Voltage at any I/O Pin	V_D	(Gnd - 0.5) to (Vdd + 0.5)	V
ESD Performance per IEC 61000-4-2 (I/O Pins)			
Air Discharge	V_{ESDIO}	19	kV
Contact Discharge	V_{ESDIO}	12	kV
ESD Performance per IEC 61000-4-2 (VDD, GND Pins)			
Air Discharge	V_{ESDPW}	30	kV
Contact Discharge	V_{ESDPW}	30	kV
Storage Temperature	T_{STG}	-55 to +150	$^\circ\text{C}$
Operating Temperature	T_{OPR}	-55 to +150	$^\circ\text{C}$

How to Order

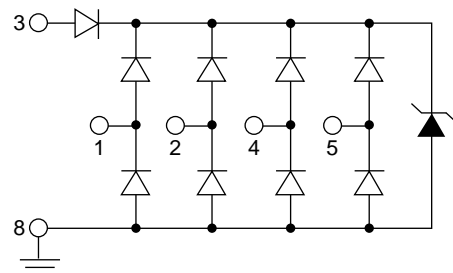
CD MSP10 - 05 04M

Common Code _____
 CD = Chip Diode
 Package _____
 MSP10 = MSOP-10 Package
 Working Peak Reverse Voltage _____
 05 = 5 V_{RWM} (Volts)
 Number of Lines _____
 04M = 4 Data Lines

Typical Part Marking

CDMSP10-0504MB0504

Schematic



WARNING Cancer and Reproductive Harm - www.P65Warnings.ca.gov

*RoHS Directive 2015/863, Mar 31, 2015 and Annex.

Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

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Applications

- High Definition Multimedia Interface (HDMI)
- Digital Visual Interface (DVI)
- Ethernet 10/100/1000 Mb/s
- SATA interface
- Portable electronics

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Electrical Characteristics (@ T_A = 25 °C Unless Otherwise Noted)

Parameter	Symbol	Minimum	Nominal	Maximum	Unit
Reverse Standoff Voltage ¹	V _{RWM}			5	V
Leakage Current ¹ @ V _{RWM}	I _D			5	μA
Channel Leakage Current @ V _{RWM}	I _{CD}			1	μA
Reverse Breakdown Voltage @ V _{RWM}	V _{BR}	6		9	V
Forward Voltage ³ @ I _F = 15 mA	V _F		0.8	1	V
ESD Clamping Voltage ² @ IEC 61000-4-2 +6 kV, Contact Mode	V _C		12.5		V
ESD Clamping Voltage ¹ @ IEC 61000-4-2 +6 kV, Contact Mode	V _C		9.5		V
ESD Dynamic Turn-On Resistance - I/O ²	R _{dyn_io}		0.33		Ohms
ESD Dynamic Turn-On Resistance - VDD ¹	R _{dyn_VDD}		0.16		Ohms
Channel Input Capacitance ² - 1 @ V _{PIN3} =5 V, V _{IN} =2.5 V, f = 1 MHz	C _{IN-1}		0.55	0.65	pF
Channel Input Capacitance ² - 2 @ V _{PIN3} =N/C, V _{IN} =2.5 V, f = 1 MHz	C _{IN-2}		0.70	0.80	pF
Channel to Channel Input Capacitance - 1 @ V _{PIN3} =5 V, V _{IN} =2.5 V, f = 1 MHz (Between channel pins)	C _{CROSS-1}		0.08	0.09	pF
Channel to Channel Input Capacitance - 2 @ V _{PIN3} =N/C, V _{IN} =2.5 V, f = 1 MHz (Between channel pins)	C _{CROSS-2}		0.10	0.11	pF
Variation of Channel Input Capacitance - 1 @ V _{PIN3} =5 V, V _{IN} =2.5 V, f = 1 MHz (Channel x Pin to GND, Channel y Pin to GND)	ΔC _{IN-1}		0.04	0.06	pF
Variation of Channel Input Capacitance - 2 @ V _{PIN3} =N/C, V _{IN} =2.5 V, f = 1 MHz (Channel x Pin to GND, Channel y Pin to GND)	ΔC _{IN-2}		0.05	0.08	pF

Notes:

1. Test from V_{dd} Pin 3 to Gnd Pin 8
2. Test from Pin 1,2,4 or 5 to Gnd Pin 8
3. Test from Gnd Pin 8 to V_{dd} Pin 3

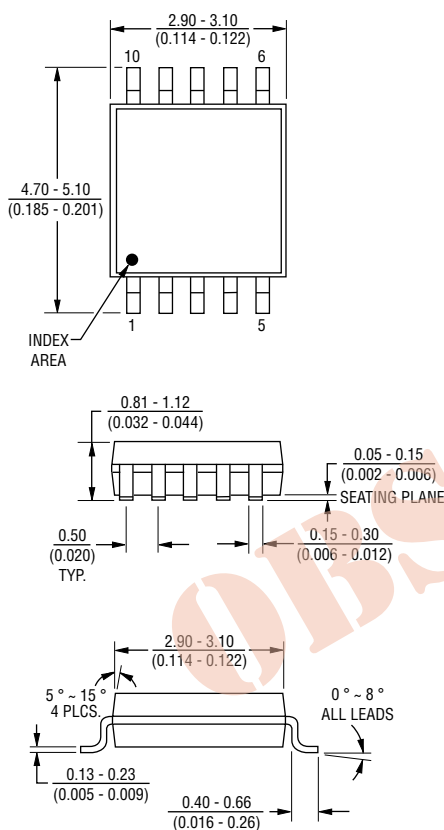
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This is a molded JEDEC MSOP-10L package with lead free 100 % Matte Sn on the lead frame. It weighs approximately 7 mg and has a flammability rating of UL 94V-0.



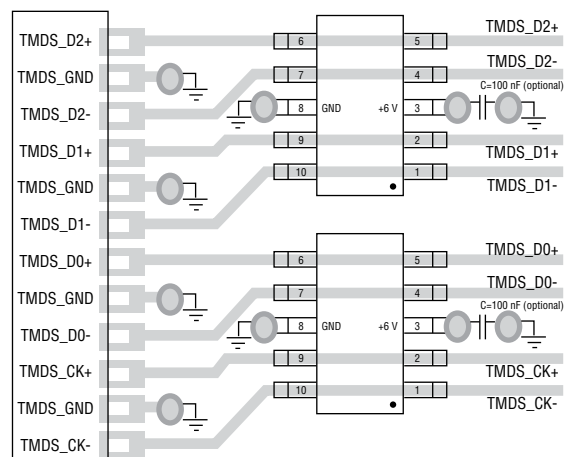
DIMENSIONS = $\frac{\text{MILLIMETERS}}{(\text{INCHES})}$

The diagram shows a 2D grid of 10 cells arranged in 5 rows and 2 columns. The cells are represented by rectangles with a '+' sign inside. The dimensions and offsets are as follows:

- Row Height:** 4.8 (offset) + 5.8 (main height) = 10.6. The offset is labeled as $\frac{4.8}{(.189)}$ and the main height as $\frac{5.8}{(.228)}$.
- Column Width:** 0.50 (main width) + 0.3 (offset) = 0.8. The main width is labeled as $\frac{1.0}{(.039)}$ and the offset as $\frac{0.50}{(.002)}$.
- Cell Spacing:** The cells are spaced by 0.189 horizontally and 0.228 vertically.

This LAND LAYOUT is for reference purposes only. Please consult your manufacturing partners to ensure your company's PCB design guidelines are met.

This ESD protection device is ideal in high speed data port protection such as HDMI where capacitance per line is critical parameter. See example connection below.



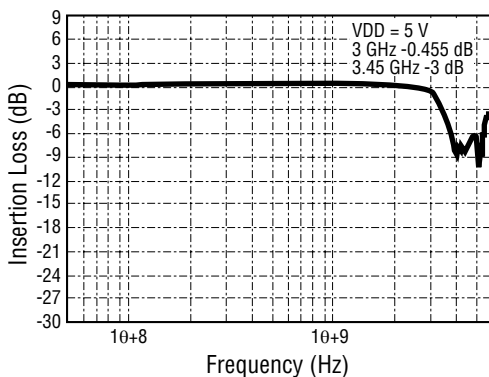
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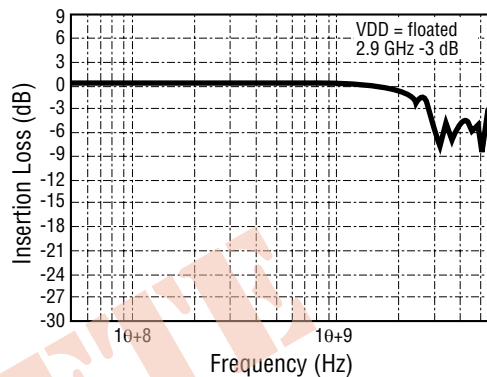
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Characteristic Curves

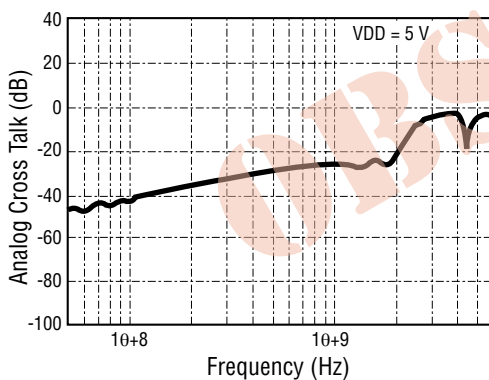
Insertion Loss S21 (I/O-to-GND)



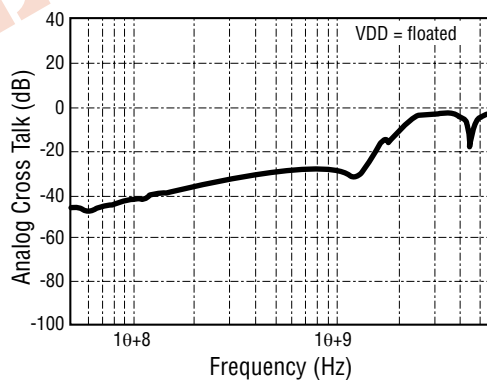
Insertion Loss S21 (I/O-to-GND)



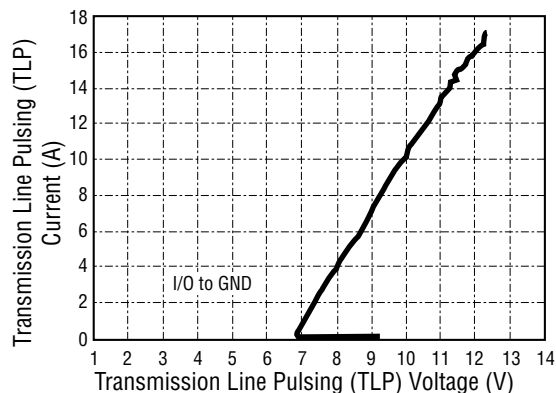
Analog Cross Talk



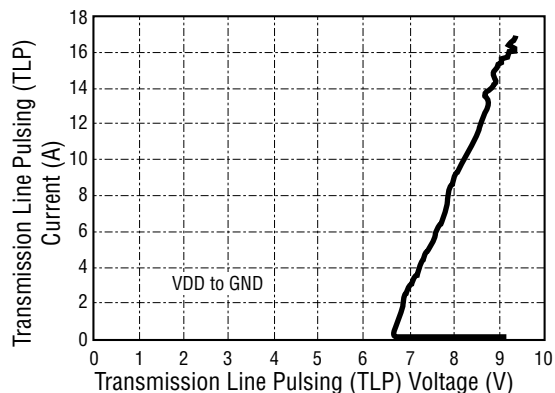
Analog Cross Talk



Transmission Line Pulsing (TLP) Measurement



Transmission Line Pulsing (TLP) Measurement



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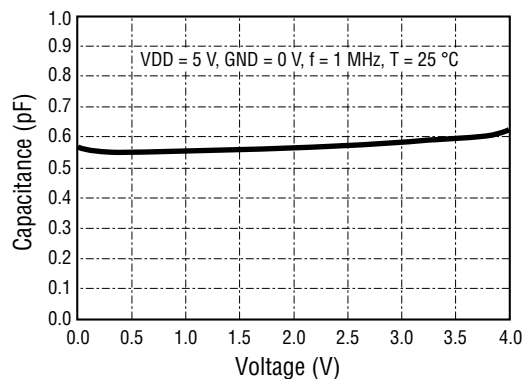
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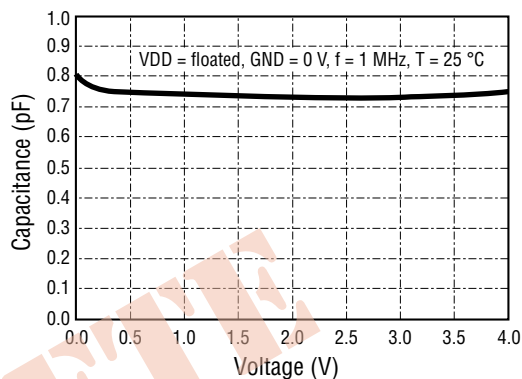
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Characteristic Curves

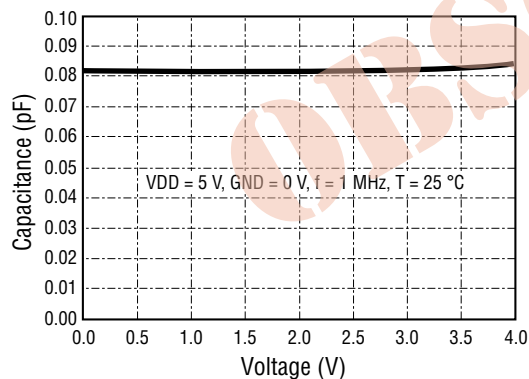
Typical Variation of C_{IN} vs. V_{IN}



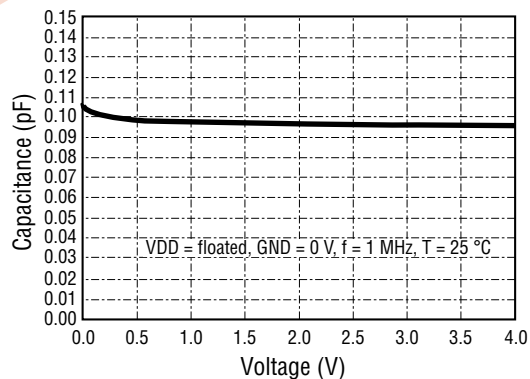
Typical Variation of C_{IN} vs. V_{IN}



Typical Variation of $C_{IO-to-IO}$ vs. V_{IN}



Typical Variation of $C_{IO-to-IO}$ vs. V_{IN}



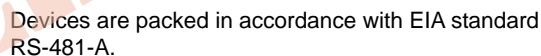
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The product will be dispensed in tape and reel format (see diagram below).



BOURNS®

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Email: eurocus@bourns.com

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www.bourns.com

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