

Features

- Available to 100K ohms
- 10 pin with 8 resistors in bussed type for pull up/down circuit
- Convex termination style
- Resistance tolerance $\pm 5\%$
- E24 Series from 10 ohms to 43K ohms
- Suitable for all types of soldering processes



This series is not recommended for new designs. See [Product Obsolescence Memo](#) for details.

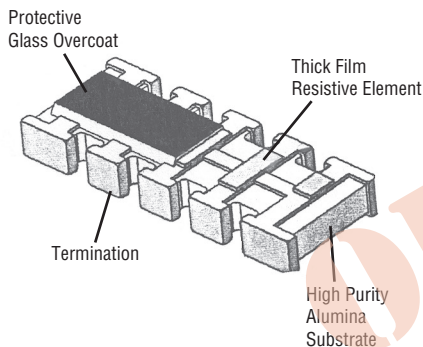
- Paper tape on plastic reel for automatic placement
- RoHS compliant*

Model CAY17 - Bussed Resistor Array

Characteristics

Number of Resistors .. 8 (bussed circuit)
 Power Rating per Resistor @ 70 °C
 0.0625 W
 Package Power Rating @ 70 °C
0.250 W
 Operating Temperature Range
 -55 °C to +155 °C
 Derated to 0 Load @.....+125 °C
 Max. Working Voltage.....25 V
 Max. Overload Voltage.....50 V
 Resistance Tolerance $\pm 5\%$
 Resistance Range/E24 Series
 10 ohms to 100K ohms
 T.C.R..... ± 250 ppm/°C

Construction



Additional Information

Click these links for more information:



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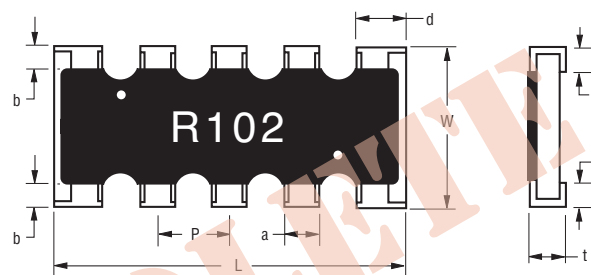


[SAMPLES](#)



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Product Dimensions



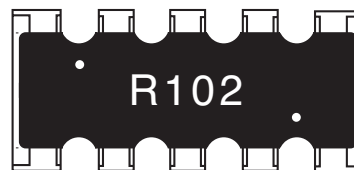
DIMENSIONS: $\frac{\text{MM}}{\text{(INCHES)}}$

Model	L	W	t	p
CAY17-JA	3.20 ± 0.20 (0.126 \pm 0.008)	1.60 ± 0.15 (0.063 \pm 0.006)	0.50 ± 0.10 (0.020 \pm 0.004)	0.64 ± 0.05 (0.126 \pm 0.002)

Model	a	b	c	d
CAY17-JA	0.35 ± 0.20 (0.014 \pm 0.008)	0.30 ± 0.20 (0.012 \pm 0.008)	0.30 ± 0.20 (0.012 \pm 0.008)	0.50 ± 0.10 (0.020 \pm 0.004)

Typical Part Marking

CAY17-JA



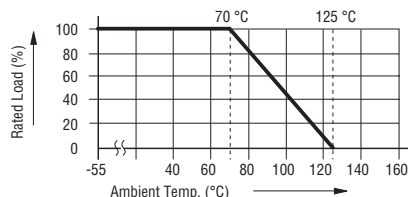
How To Order

CA Y 17 - 103 J A LF

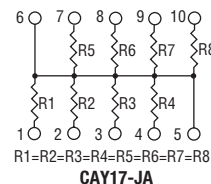
- Chip Arrays ————
- Type ————
- Y = Convex
- Model ————
- 17 = 1206 Package Size
- Resistance Code ————
- <10 ohms: "R" represents decimal point (example: 4R7 = 4.7 ohms).
 - ≥ 10 ohms: First two digits are significant, third digit represents number of zeros to follow (example: 474 = 470k ohms)
- Resistance Tolerance ————
- J = $\pm 5\%$
- Resistors ————
- A = Common from terminal 5 to 10
- Terminations ————
- LF = Tin-plated (RoHS compliant)

For Standard Values Used in Capacitors, Inductors, and Resistors, [click here](#).

Derating Curve



Bussed Circuit



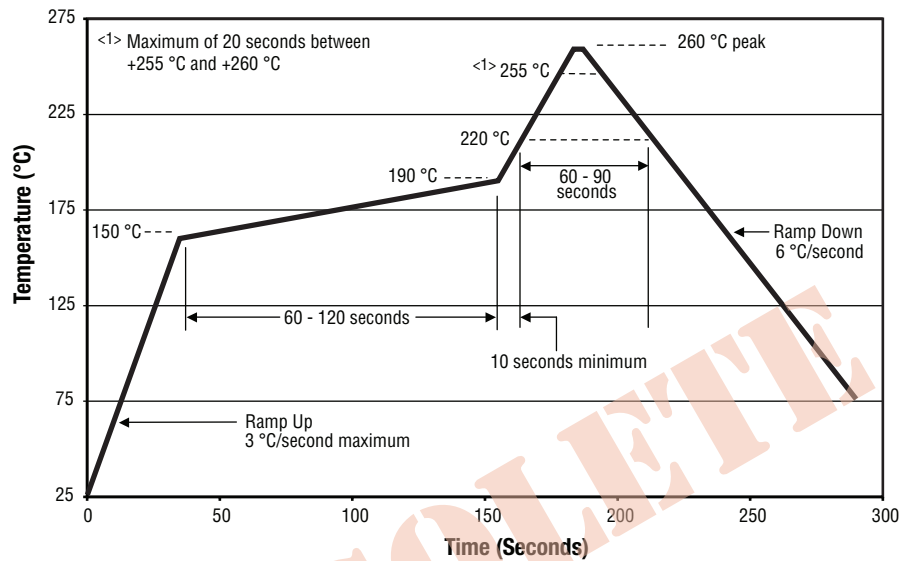
WARNING Cancer and Reproductive Harm
www.P65Warnings.ca.gov

*RoHS Directive 2015/863, Mar 31, 2015 and Annex.
 Specifications are subject to change without notice.
 Users should verify actual device performance in their specific applications.
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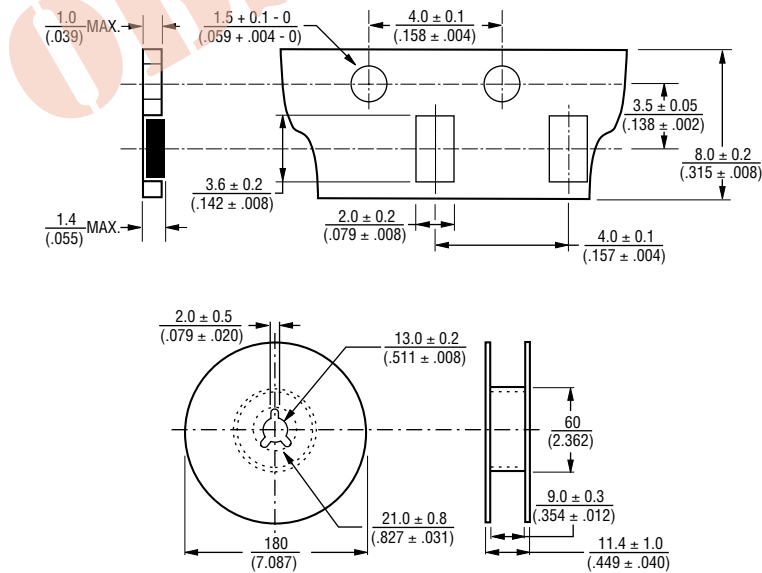
Model CAY17 - Bussed Resistor Array

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Soldering Profile for RoHS Compliant Chip Resistors and Arrays



Packaging Dimensions



DIMENSIONS: $\frac{\text{MM}}{(\text{INCHES})}$

REV. 10/23

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Chip Resistor Arrays - Application Note

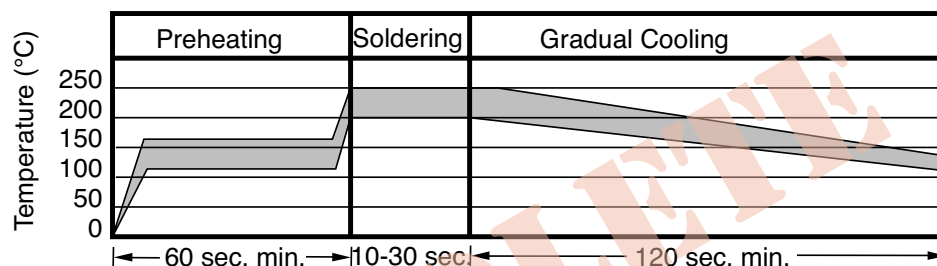
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Component Placement

- Reduce the mechanical stress to a minimum during and after placing of the unit in order not to damage the terminals and protective coating.
- Misplacement of components may cause solder bridges.

Soldering

- Reflow soldering: Recommendation is shown in the following chart.
- Wave soldering: Recommendation according to IEC standards.
- Hand soldering: Don't touch the protective coating of the part. Solder within 3 seconds when the temperature is over 280 °C.



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