

## Features

- Lead free as standard
- RoHS compliant\*
  ESD protection
- Protects up to ten data lines
- Low insertion loss

## **Applications**

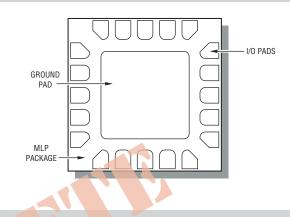
- Cell Phones
- PDAs and Notebooks
- GPS and SMART Cards

# 2FAB-M20R – Integrated Passive & Active Device using MLP

### **General Information**

The 2FAB-M20R device, manufactured using Thin Film on Silicon technology, provides ESD protection for the external ports of portable electronic devices such as cell phones, modems and PDAs.

The ESD protection provided by the component enables a data port to withstand a minimum  $\pm 8$  KV Contact / $\pm 15$  KV Air Discharge per the ESD test method specified in IEC 61000-4-2. The device measures 3.5 mm x 3.5 mm and is intended to be mounted directly onto an FR4 printed circuit board. The MLP device meets typical thermal cycle and bend test specifications.



### **Electrical & Thermal Characteristics**

Electrical Characteristics ( $T_{a} = 25 \text{ °C}$ unless otherwise noted)	Symbol	Minimum	Nominal	Maximum	Unit
Resistance	R	90	100	110	Ω
Capacitance @ 2.5 V 1 MHz	С	16	20	24	pF
Rated Standoff Voltage	Vwm		5.0		V
Breakdown Voltage @ 1 mA	VBR	6.0			V
Forward Voltage @ 10 mA	VF		0.8		V
Leakage Current @ 3 V	Ŀ			0.1	μΑ
ESD Protection: IEC 61000-4-2 Contact Discharge Air Discharge		±8 ±15			kV kV
<b>Thermal Characteristics</b> ( $T_{*} = 25 \text{ °C unless otherwise noted}$ )					
DC Power Rating	Р			100	mW
Operating Temperature Range	T.	-40	25	+85	°C
Storage Temperature Range	Ты	-55	25	+150	°C

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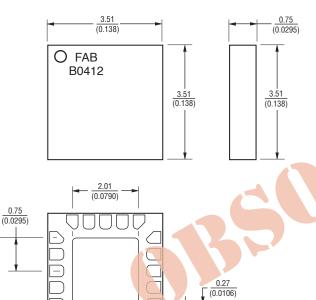
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#### **Product Dimensions**

2.03

(0.080)

This silicon-based device is packaged using micro leadframe packaging technology. The MLPs have an exposed die attach pad that provides the interconnect medium from die to PCB. The pads are arranged for easy PCB routing. The pitch is 0.5 mm and the dimensions for the packaged device are shown below.

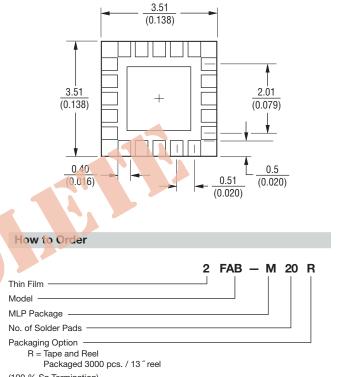


DIMENSIONS = MILLIMETERS

(INCHES)

0.43

### **Recommended Pad Layout**



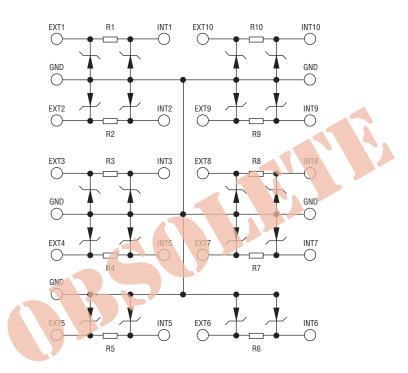
(100 % Sn Termination)

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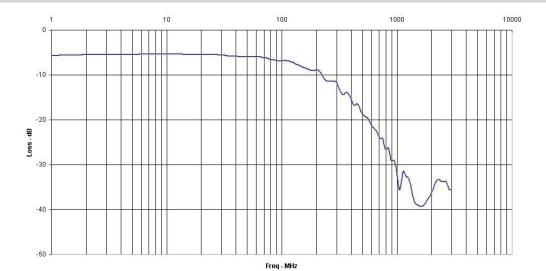
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### **Block Diagram**

The MLP Device block diagram below includes the pin names and basic electrical connections associated with each channel.



### **Frequency Response**

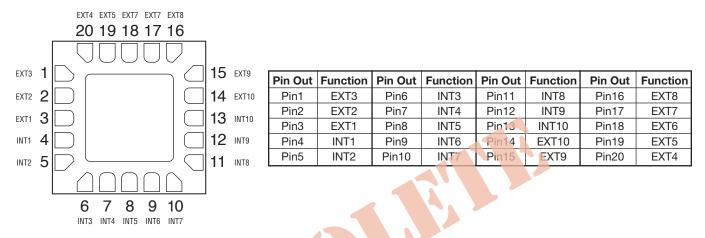


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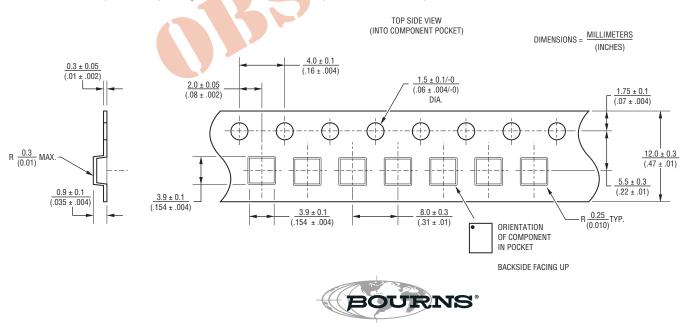
### **Device Pin Out**

The Pin-Out for the device is shown below. Note also that the device is shown with bottom side pads facing up.



### Packaging

The surface mount product is packaged in a 12 mm × 8 mm Tape and Reel format per EIA-481 standard.



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